REVISIONS				
ZONE	REV.	DESCRIPTION	DATE	APPROVED
	A	RELEASE	7/27/93	
	B	CHG'D WAH & COMP PER REV B PCB	9/3/93	
	C	REVRS'D R237, D221	9/15/93	

INPUTS

REAR PANEL

FRONT PANEL

Open frame jack

(MINIMUM GAIN = +18V, MAXIMUM GAIN = 0V)

(LOWEST THRESHOLD = 0V, HIGHEST THRESHOLD = +18V)

DOOR_SLAMMER

CV9 WAH SENSITIVITY

CV12 WAH DECAY

CV15 PEDAL

COMP_CONT

WAH_CONT-

WAH_CONT+

CLK1

DAT1

STB3

T_CONTROL_1

T_CONTROL_2

T_CONTROL_7

T_CONTROL_6

T_CONTROL_4

T_CONTROL_5

T_CONTROL_8

T_CONTROL_3

TO TUBE STAGE

MP2B_OUT

WAH-

WAH+

FROM AM384 (LEVEL SHIFTER CKTS ON MP2C)

COMP_CONT

MP2 SCHEMATIC LIST		
DWG	TITLE	FILE
D850	MICROPROCESSOR AND INTERFACE	MP2A
D851	INPUT PREAMP, COMPRESSOR, WAH	MP2B
D852	TUBE CKTS, MASTER VOLUME	MP2C
D853	PASSIVE AND ACTIVE EQ	MP2D
D854	CHORUS, EFFECTS LOOP, NOISE GATE	MP2E
D855	OUTPUT MIXERS, DRVRS AND CONTROLS	MP2F
D856	LED METER AND POWER SUPPLY	MP2G

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ADA SIGNAL PROCESSORS, INC
420 LESSER STREET
OAKLAND, CA 94601

MP-2 SCHEMATIC: INPUT STAGE, COMP/WAH FILTER

DATABASE FILE MP2B.DBF

DRAWN: EMB

ENGR: EMB/RTS/DJT

CHECKED BY:

APPROVED:

SCALE:

DATE: 8-SEP-93

SIZE: D

DRAWING NUMBER: 851

SHEET

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